



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gerd Frankowsky et al.
Serial No. : 10/630,632
Filed : July 29, 2003

Art Unit : 2813
Examiner : James M. Mitchelle
Confirmation No.: 9058

Notice of Allowance Date: June 6, 2005

Title : SEMICONDUCTOR CIRCUIT MODULE AND METHOD FOR FABRICATING
SEMICONDUCTOR CIRCUIT MODULES

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER ALLOWANCE

gk
to
dated
GMM
9/14/06

In response to the supplemental notice of allowability mailed December 12, 2005, please
amend the application as indicated on the following pages.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being
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